WHAT IS CLAIMED IS:

1. A method for averaging two pixel values, comprising:

decoding an instruction;

loading a plurality of first operands from a first input register;

loading a plurality of second operands from a second input register;

producing an average of one of the plurality of first operands and one of the plurality of second operands; and

storing the average in an output register.

2. The method of claim 1, determining how many fields are in each of the first and second input registers.

3. The method of claim 1, wherein the producing the average comprises:

producing a first intermediate result by adding one of the plurality of first operands to one of the plurality of second operands; and

producing the average by shifting the first intermediate result to the right by one binary digit.

4. The method of claim 1, wherein the producing the average comprises:

producing a first intermediate result by adding one of the plurality of first operands, one of the plurality of second operands and a rounding factor; and

producing the average by shifting the first intermediate result to the right by one binary digit.

5. The method of claim 1, further comprising rounding the average before storing the average.

6. The method of claim 1, further comprising:

evaluating a rounding factor; and

adding a value to the average.

7. The method of claim 7, wherein the value is one of zero and one.

8. A method for averaging two pixel values, comprising:

decoding an instruction;

loading a first operand from an A1 field of a first input register;

loading a second operand from a B1 field of a second input register;

producing an average of the first operand and the second operand; and

storing the average in a C1 field of an output register.

9. The method of claim 8, wherein the instruction is one of a plurality of instructions in a long instruction word.

10. The method of claim 8, determining how many fields are in each of the first and second input registers.

11. The method of claim 8, wherein the producing an average comprises:

producing a first intermediate result by adding one of the plurality of first operands to one of the plurality of second operands; and

producing the average by shifting the first intermediate result to the right by one binary digit.

12. The method of claim 8, wherein the producing an average comprises:

producing a first intermediate result by adding one of the plurality of first operands, one of the plurality of second operands and a rounding factor; and

producing the average by shifting the first intermediate result to the right by one binary digit.

13. The method of claim 8, further comprising:

evaluating a rounding factor; and

adding a value to the average.

14. The method of claim 13, wherein the value is one of zero and one.

15. The method of claim 8, wherein the first input register comprises a plurality of fields.

16. The method of claim 8, further comprising rounding the average before storing the average.

17. The method of claim 8, further comprising:

loading a third operand from an A2 field of the first input register;

loading a fourth operand from a B2 field of the second input register;

producing a second average of the third operand and the fourth operand; and

storing the second average in a C2 field of the output register.

18. A pixel averaging apparatus, comprising

a first input register comprising a plurality of first fields;

a second input register comprising a plurality of second fields;

a plurality of average modules respectively coupled to the first and second fields; and

an output register comprising a plurality of third fields, wherein the third fields are respectively coupled to the plurality of average modules.

19. The pixel averaging apparatus of claim 18, wherein the average module comprises:

a plurality of adders respectively coupled to the first and second fields; and

a plurality of shifters respectively coupled to the plurality of adders.

20. The pixel averaging apparatus of claim 18, further comprising a rounding factor that causes at least one of rounding-up and rounding-down.

21. The pixel averaging apparatus of claim 18, wherein a rounding factor is added to the first and second fields in the average module.